File View Edit Tools Window Help

Drafts
  BRS:
Pending
Active
  L1: (2) 6075932.pn.
  L2: (2) 5696694.pn.
  L3: (2) 5682320.pn.
  L4: (2) 5668732.pn.
  L5: (183) (electrical adj circuit) and (neur$4 adj net$4)
  L6: (22) (electrical adj circuit) same (neur$4 adj net$4)
  L7: (1) neur$4 adj net$4 adj electrical adj circuit
  L8: (26671) neur$4 same net$4 model$1 same electrical same circuit
  L9: (3) (neur$4 adj net$4) same (electrical adj circuit) same model$1
  L10: (0) neur$4 adj net$4 adj power adj supply
Failed
Saved
Favorites
Tagged (2)
UDC
Queue
Trash

3/19/04

reviewed all

U	Document ID	Issue Dat	Pages	Title	Current OR	Current XR	Retrieval	Inventor	S	C	P		3		Im

Ready

◈IEEE

IEEE Xplore®
RELEASE 1.6

Membership Publications/Services Standards Conferences Careers/Jobs

Welcome
United States Patent and Trademark
Office

IEEE Xplore®
1 Million Documents
1 Million Users
...And Growing

Help FAQ Terms IEEE Quick Links ▼   » **Search Results**
Peer Review

Welcome to IEEE Xplore

○ Home
○ What Can
I Access?
○ Log-out

Tables of Contents

○ Journals
& Magazines
○ Conference
Proceedings
○ Standards

Search

○ By Author
○ Basic
○ Advanced

Member Services

○ Establish IEEE
Web Account
○ Access the
IEEE Member
Digital Library

Your search matched **26** of **1013964** documents.
A maximum of **500** results are displayed, **15** to a page, sorted by
Relevance in **Descending** order.

Refine This Search:
You may refine your search by editing the current search
expression or entering a new one in the text box.

(new(phrase) ret*) and (electrical<phrase> circuit)*

Search

☐ Check to search within this result set

Results Key:
JNL = Journal or Magazine   **CNF** = Conference   **STD** = Standard

---

1 **Fault diagnosis in electronic circuits based on bilinear transformation in 3-D and 4-D spaces**
Czaja, Z.; Zielonko, R.;
Instrumentation and Measurement, IEEE Transactions on
, Volume: 52 , Issue: 1 , Feb. 2003
Pages:97 - 102

[Abstract]   [PDF Full-Text (546 KB)]   **IEEE JNL**

---

2 **Using wavelet neural networks for the optimal design of electromagnetic devices**
Wu Qing; Shen Xueqin; Yang Qingxin; Yan Weili;
Magnetics, IEEE Transactions on , Volume: 33 , Issue: 2 , March 1997
Pages:1928 - 1930

[Abstract]   [PDF Full-Text (328 KB)]   **IEEE JNL**

---

3 **A MOS circuit for a nonmonotonic neural network with excellent retrieval capabilities**
Asai, T.; Yokotsuka, H.; Fukai, T.;
Neural Networks, IEEE Transactions on , Volume: 7 , Issue: 1
, Jan. 1996
Pages:182 - 189

[Abstract]   [PDF Full-Text (712 KB)]   **IEEE JNL**

---

4 **5th Biennial IEEE Conference on Electromagnetic Field Computation (CEFC)**

Magnetics, IEEE Transactions on , Volume: 29 , Issue: 2 , Mar